	Туре	L	#	Hits	Search Text	DBs	Time Stamp	Comment
1	BRS	L1		H871	distribut\$5 adj1 memory	USPAT	2005/04/1 1 13:52	
2	BRS	L2		5424	1 adn (source adj1 port)	USPAT	2005/04/1 1 13:52	
3	BRS	L3		31	1 and (source adj1 port)	USPAT	2005/04/1 1 13:53	
4	BRS	L4		30	3 and destination	USPAT	2005/04/1 1 13:53	
5	BRS	L5		1	4 and (memory adj1 pool)	USPAT	2005/04/1 1 13:53	
6	BRS	L6		27	4 and packets	USPAT	2005/04/1 1 13:53	
7	BRS	L7		5	6 and ASIC	USPAT	2005/04/1 1 13:53	
8	BRS	L8		4	7 and stage	USPAT	2005/04/1 1 13:54	